

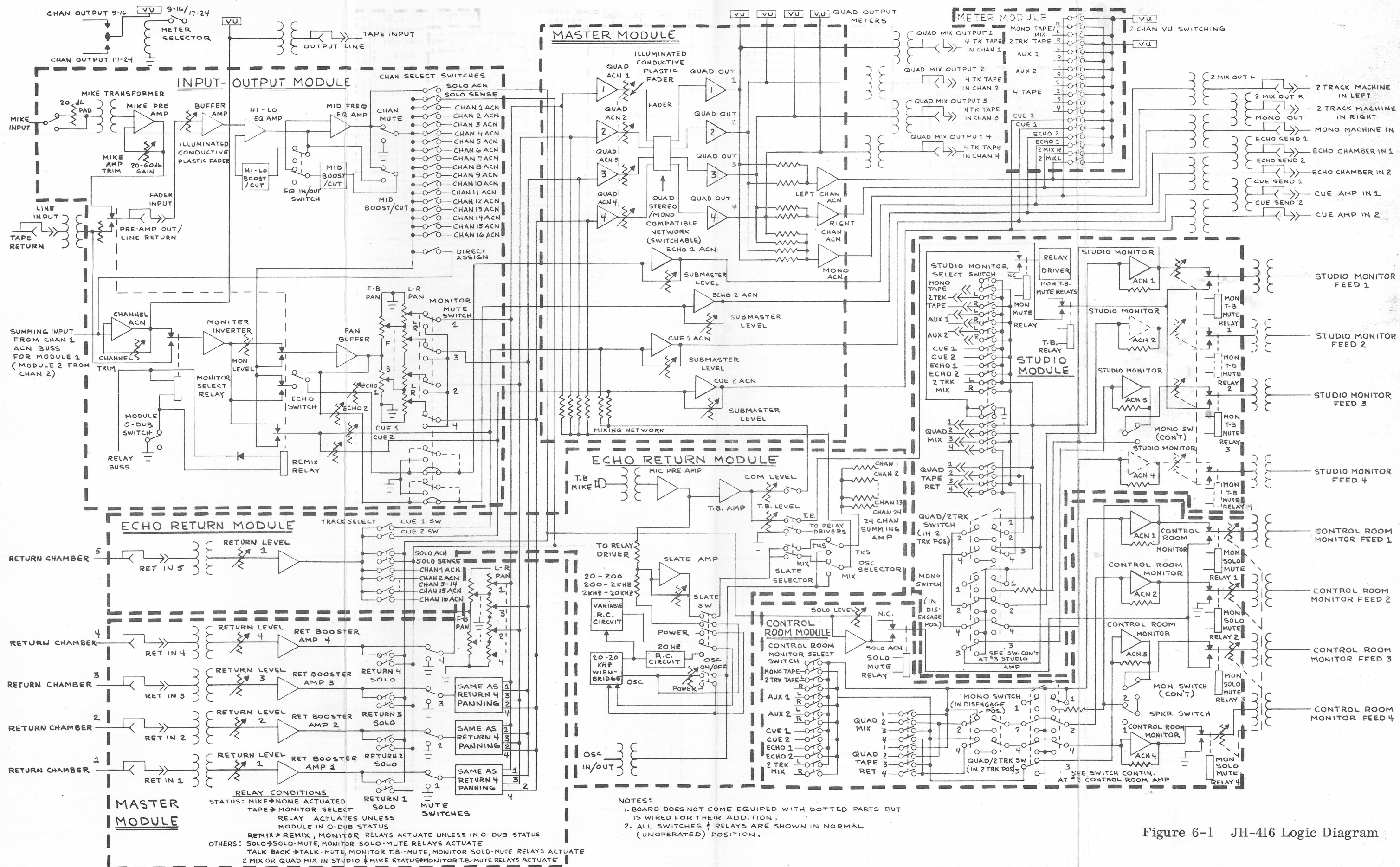


Figure 6-1 JH-416 Logic Diagram

NOTE
UNLESS SPECIFIED ALL RESISTORS ARE .5% 5W
ALL CAPACITORS ARE 25VDC

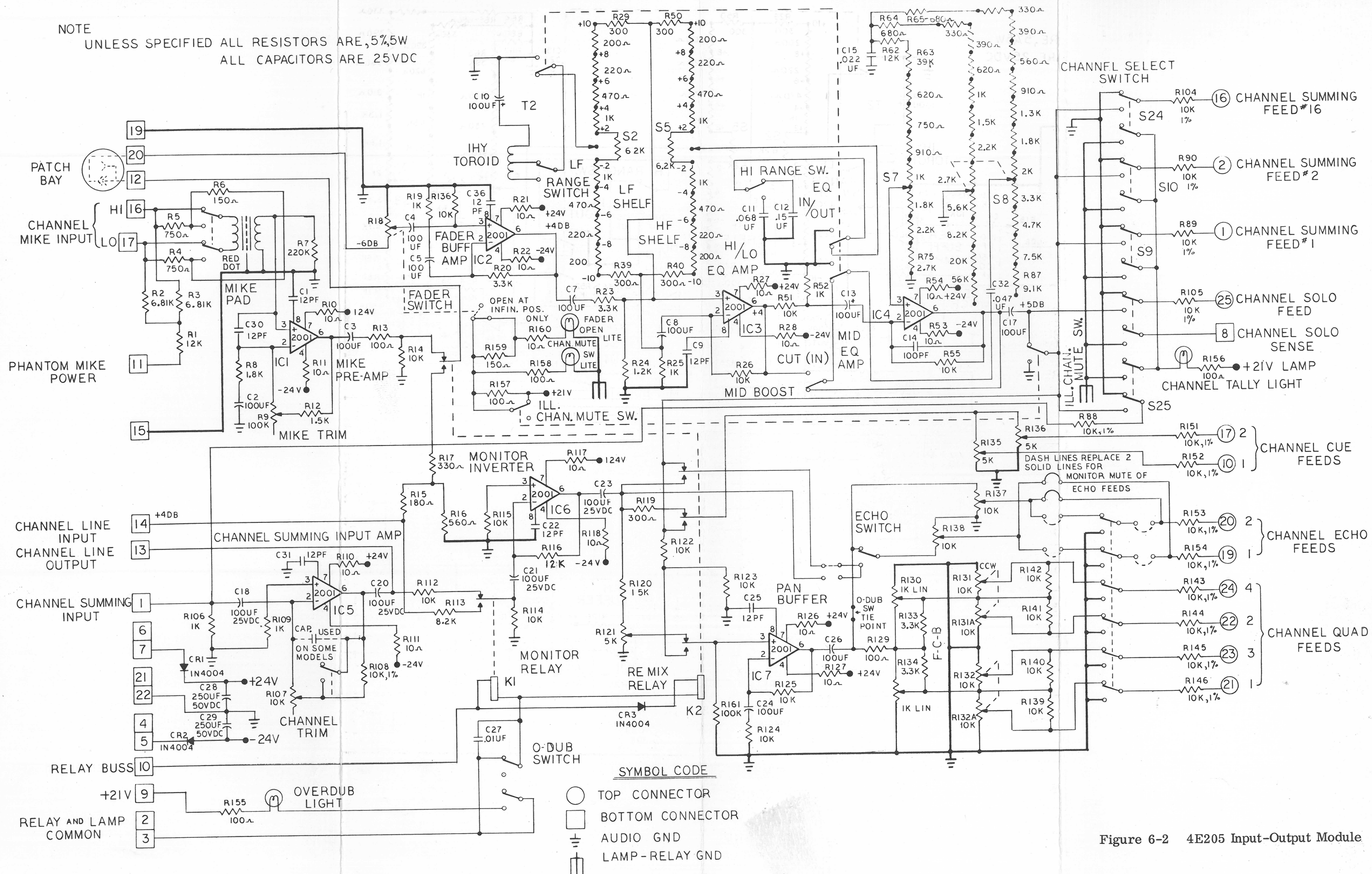


Figure 6-2 4E205 Input-Output Module



Figure 6-3 4E244 Auxillary Module

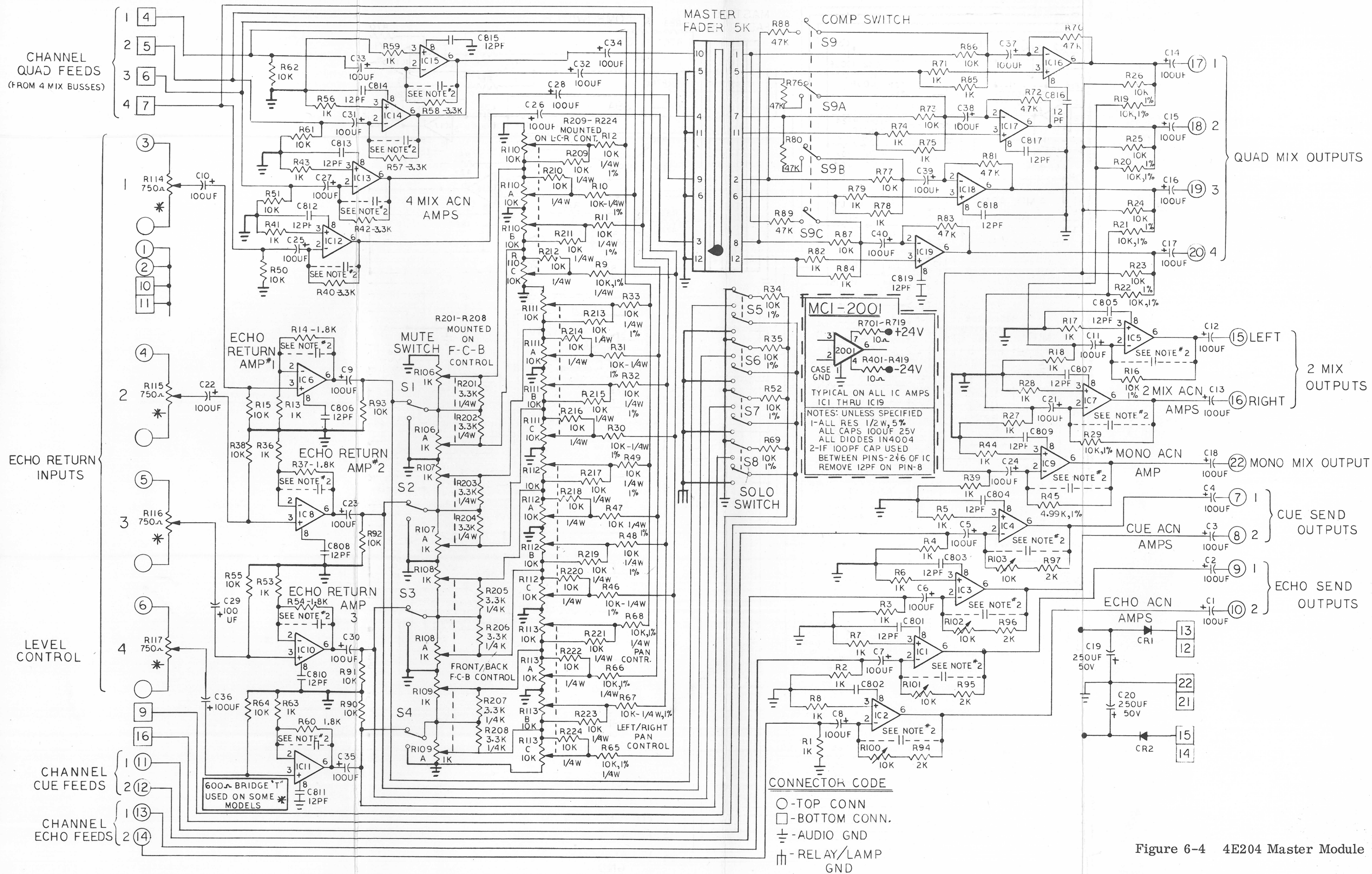


Figure 6-4 4E204 Master Module

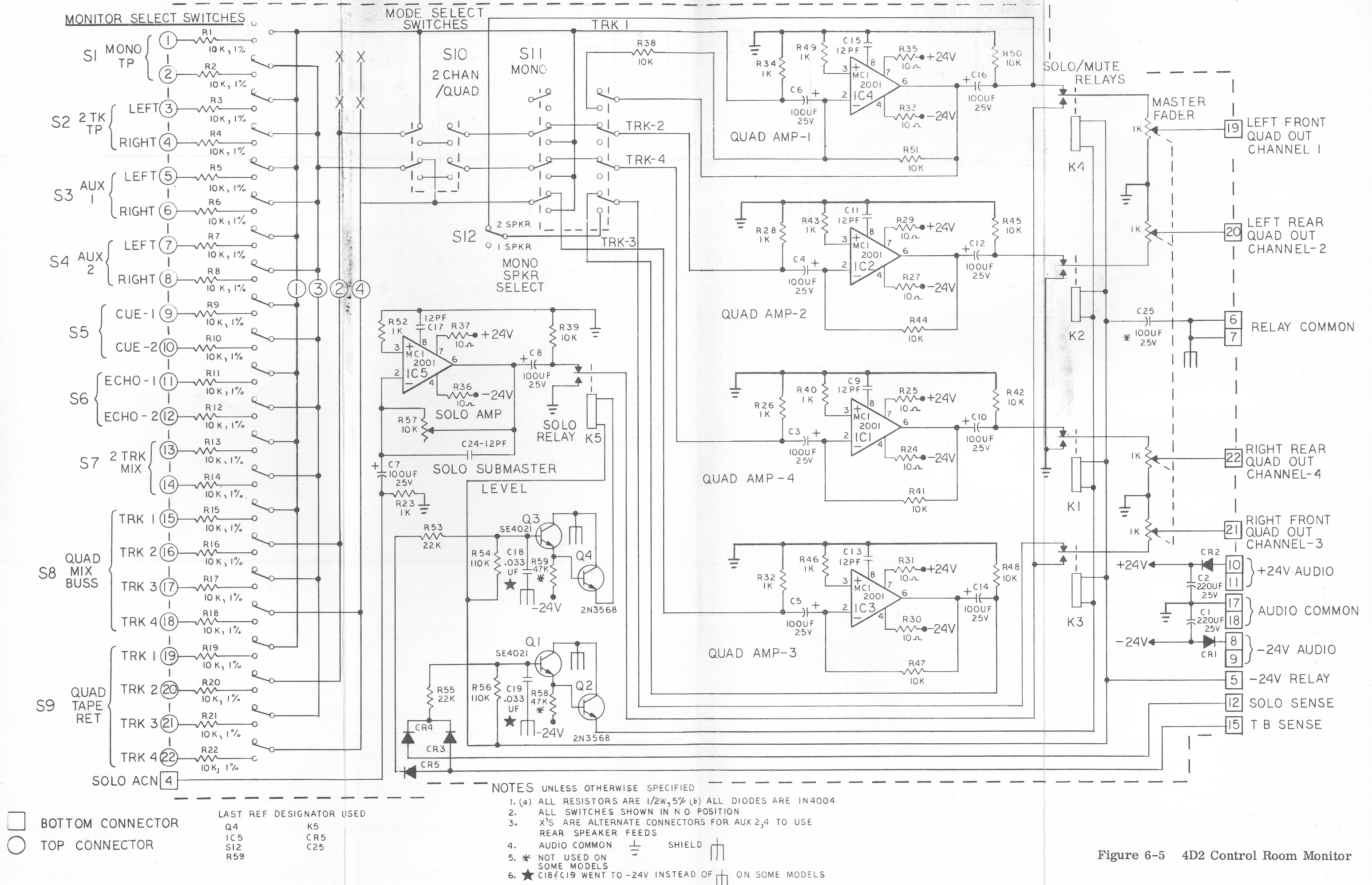


Figure 6-5 4D2 Control Room Monitor

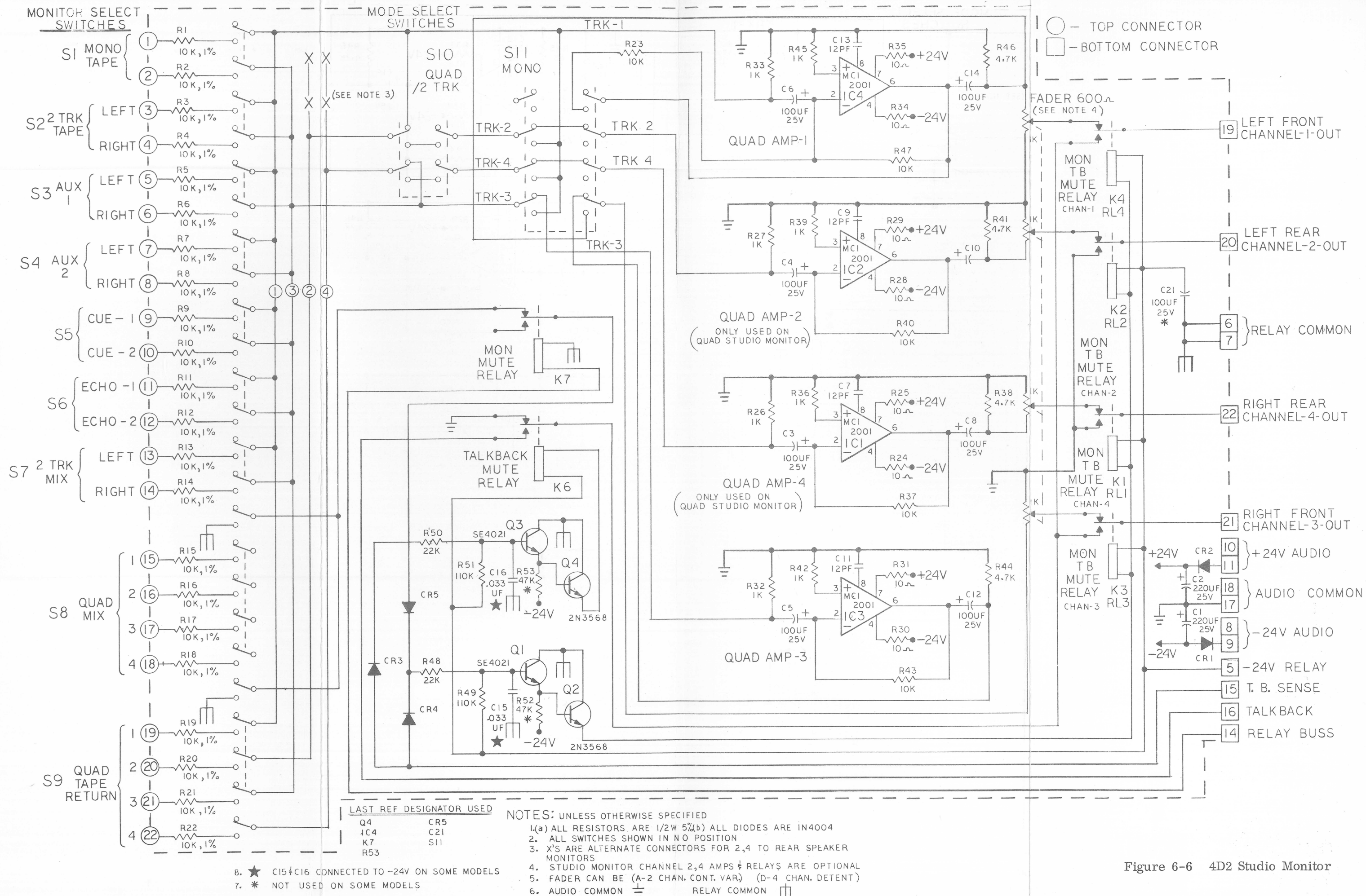


Figure 6-6 4D2 Studio Monitor

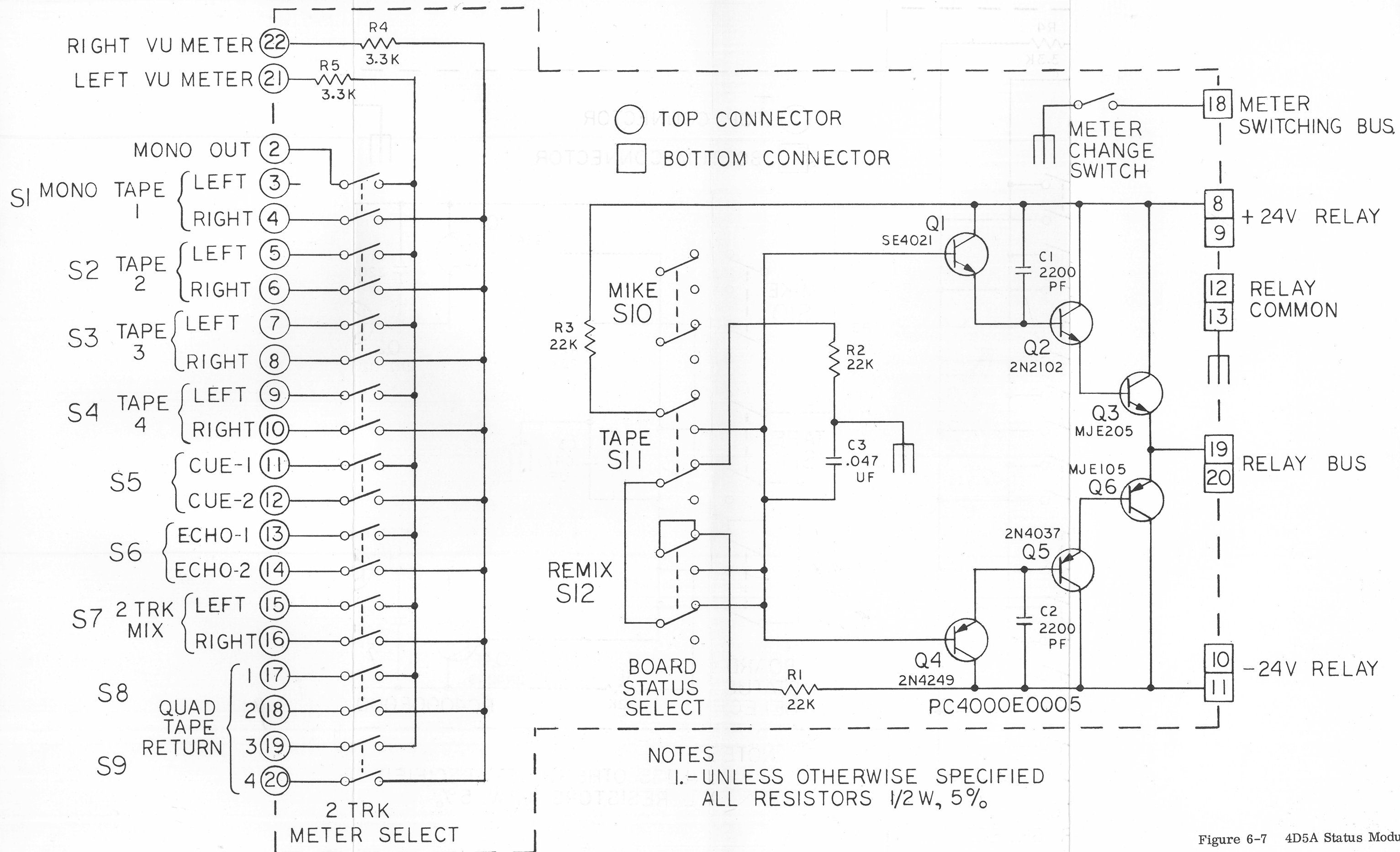
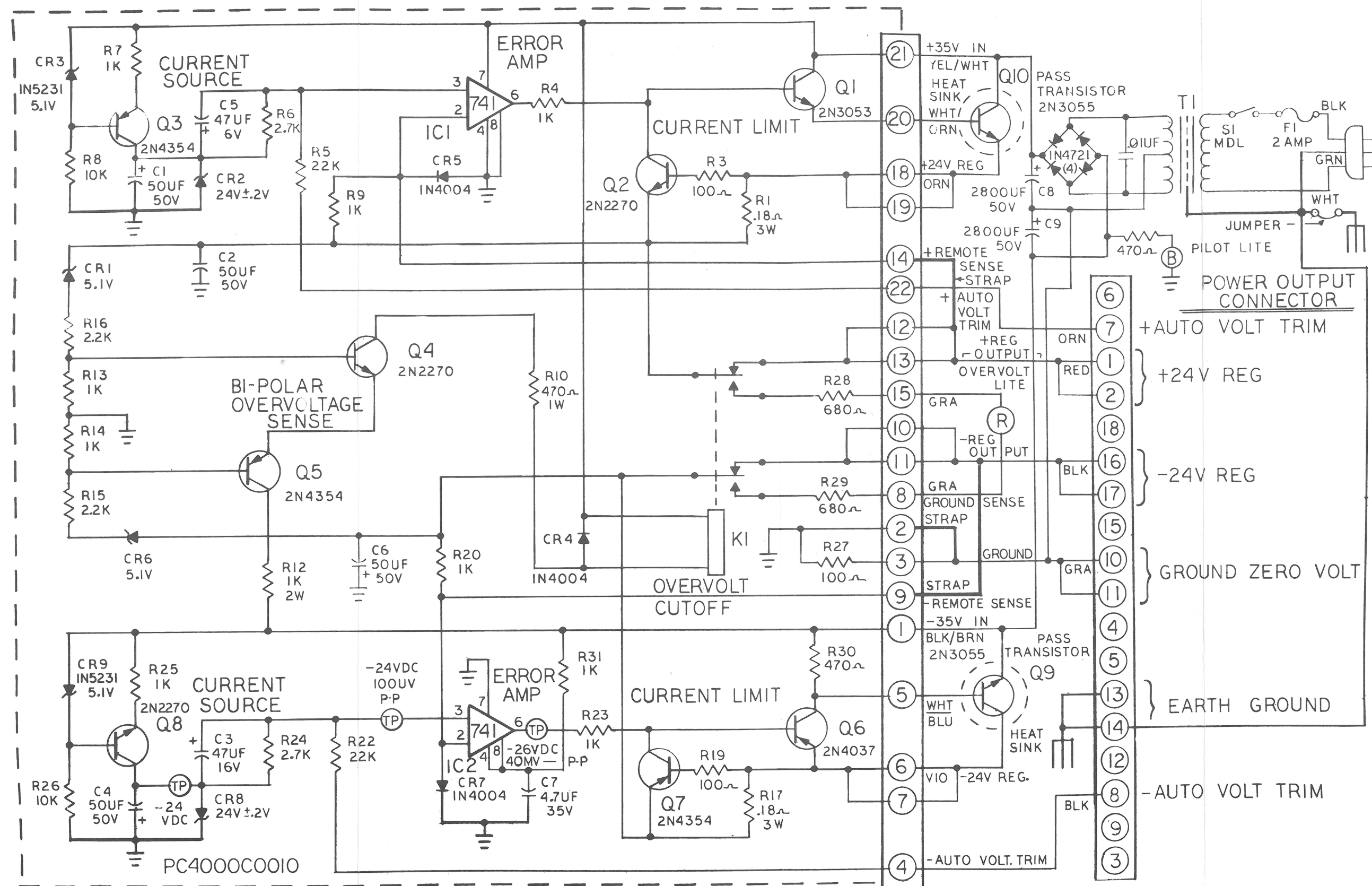


Figure 6-7 4D5A Status Module



- NOTES: UNLESS OTHERWISE SPECIFIED
- 1.- ALL RESISTORS ARE 1/2 WATT 5%
 - 2.- ALL CAPACITORS ARE UF
 - 3.- TO CONNECT ZERO VOLTS BUSS TO EARTH GND, JUMPER PIN 10 TO 13 ON POWER OUTPUT CONN.
 - 4.- TO TRIM VOLTAGE TO $\pm 22V$, JUMPER PINS 7, 8 & 10, 11 ON POWER OUTPUT CONNECTOR.

Figure 6-8 JH-416 Power Supply